

Please also amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel Claims ~~38-60~~ and ~~69-75~~.

Please add the following new claims:

78 (New). A light emitting device comprising: a source signal line driver circuit; a first gate signal line driver circuit; a second gate signal line driver circuit; a pixel portion; a plurality of source signal lines connected to said source signal line driver circuit; a plurality of first gate signal lines connected to said first gate signal line driver circuit; a plurality of second gate signal lines connected to said second gate signal line driver circuit; and a plurality of power source supply lines;

wherein said pixel portion comprises a plurality of pixels;

wherein each of said plurality of pixels comprises a switching TFT, a first EL driver TFT, a second EL driver TFT, an erasure TFT, and an EL element;

wherein said first EL driver TFT and said second EL driver TFT are connected in parallel;

wherein a gate electrode of said switching TFT is electrically connected to said first gate signal line driver circuit through at least one of said plurality of first gate signal lines;

wherein one of a source region and a drain region of the switching TFT is electrically connected to said source signal line driver circuit through at least one of said plurality of source signal lines, and the other of said source region and said drain region is electrically connected to a gate electrode of said first EL driver TFT through at least a gate electrode of said second EL driver TFT;

wherein a gate electrode of said erasure TFT is electrically connected to said second gate signal line driver circuit through at least one of said plurality of second gate signal lines;

wherein a gate electrode of said erasure TFT is electrically connected to said second gate signal line driver circuit through at least one of said plurality of second gate signal lines;

wherein one of a source region and a drain region of said erasure TFT is electrically connected to one of said plurality of power source supply lines, and the other of said source region and said drain region is electrically connected to said gate electrode of said first EL driver TFT through at least said gate electrode of said second EL driver TFT;

wherein each of a source region of said first EL driver TFT and a source region of said second

EL driver TFT is electrically connected to said power source supply line; and

wherein each of a drain region of said first EL driver TFT and a drain region of said second

EL driver TFT is electrically connected to said EL element.

79 (New). A light emitting device according to claim 78, wherein said first EL driver TFT and said second EL driver TFT have the same polarity.

80 (New). A light emitting device according to claim 78, wherein at least one TFT, selected from the group consisting of said switching TFT, said erasure TFT, said first EL driver TFT, and said second EL driver TFT, is a top gate TFT.

81 (New). A light emitting device according to claim 78, wherein at least one TFT, selected from the group consisting of said switching TFT, said erasure TFT, said first EL driver TFT, and said second EL driver TFT, is a bottom gate TFT.

82 (New). A light emitting device according to claim 78, wherein said source signal line driver circuit comprises a shift register, a first latch, and a second latch.

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83 (New). A light emitting device according to claim 82, wherein one latch selected from the group consisting of said first latch and said second latch comprises two clocked invertors and two invertors.

84 (New). A light emitting device according to claim 78, wherein one EL driver selected from the group consisting of said first EL driver TFT and said second EL driver TFT is driven in a linear region.

85 (New). A light emitting device according to claim 78, wherein said light emitting device is a device selected from the group consisting of a computer, a video camera, and a DVD player.

86 (New). A light emitting device according to claim 78,
wherein said light emitting device comprises a gate wiring connected to said gate electrode of said first EL driver TFT and to said gate electrode of said second EL driver TFT, and a capacitor wiring connected to said power source supply line; and

wherein a gate insulating film of said switching TFT, said erasure TFT, said first EL driver TFT, and said second EL driver TFT is provided between said gate wiring and said capacitor wiring.

87 (New). A light emitting device according to claim 78,

wherein, from among said plurality of pixels, two pixels along the direction in which said plurality of first gate signal lines are formed, are adjacent with one of said plurality of power source supply lines therebetween; and

wherein a source region of said first EL driver TFT and a source region of said second EL driver TFT of each of said two pixels are connected to said power source supply line.

88 (New). A light emitting device according to claim 78,

wherein two pixels along the direction in which said plurality of first gate signal lines are formed, are adjacent with one of said plurality of said second gate lines therebetween;

wherein a gate electrode of said first EL driver TFT and a gate electrode of said second EL driver TFT of each of said two pixels are connected to said plurality of second gate signal lines; and

wherein a source region of said first EL driver TFT and a source region of said second EL driver TFT of each of said two pixels are connected to one of said plurality of power source supply lines.

89 (New). A light emitting device according to claim 78, wherein said plurality of first gate signal lines and said plurality of second gate signal lines are formed in parallel.

90 (New). A light emitting device according to claim 89, wherein said plurality of first gate signal lines and said plurality of second gate signal lines overlap with an insulating film therebetween.

91 (New). A light emitting device according to claim 78, wherein said plurality of source signal lines and said plurality of power source supply lines are formed in parallel.

92 (New). A light emitting device according to claim 91, wherein said plurality of source signal lines and said plurality of power source supply lines overlap with an insulating film therebetween.

93 (New). A light emitting device according to claim 78, wherein said plurality of first gate signal lines and said plurality of power source supply lines are formed in parallel.

94 (New). A light emitting device according to claim 93, wherein said plurality of first gate signal lines and said plurality of power source supply lines overlap with an insulating film therebetween.

95 (New). A light emitting device according to claim 78, wherein said plurality of second gate signal lines and said plurality of power source lines are formed in parallel.